

# ENHANCING POWER QUALITY IN PV FED MICROGRID BY USING UAPF

ELLA SRAVYA<sup>1</sup>|S NAGANNA<sup>2</sup> B.SHIVAJI<sup>3</sup>

<sup>1</sup>Pg Scholar, Dept of EPS, Kodada Institute of Technology & Science for Women-Kodad, Telangana.

<sup>2</sup> Assistant Professor, Dept of EEE, Kodada Institute of Technology & Science for Women-Kodad, Telangana.

<sup>3</sup>Assoc Professor & HOD, Dept of EEE, Kodada Institute of Technology & Science for Women-Kodad, Telangana.

**ABSTRACT:** In order to improve power quality indices, this study investigates the photovoltaic fed universal active power filter (PV-UAPF) system. Comb filter and second-order generalized integrator (CFSOGI) instantaneous power balance theory with power angle (IPBTPA) are the foundations of this novel control technique. To solve the challenges of flexible indent channels, the CFSOGI efficiently eliminates the stage point, recurrence, and fundamental positive and negative grouping parts (FPNSC) under distorted or adverse lattice settings. In this case, a brush channel is a collection of zeros at various symphonic frequencies that creates an indent beat and provides the prevailing consonant excusal limit. Despite all of that solar-powered PV, IPBTPA generates the reference signs to operate the structure at a twofold compensation approach. The shunt converter's burden is decreased by using the power angle to extract the desired load voltages during grid voltage changes. Furthermore, the hysteresis regulator estimates the reference and load powers to produce the varying beats. For exceptional performance, this regulator upholds the framework power balance and effective DC voltage guideline. To further assist the system, responsive power compensation, symphonious stream coverage, and grid voltage change lightening are also completed. In order to understand a microgrid system, the structure is also tried in network voltage obstruction for islanding mode. Propagation findings are also used to support the overall system execution under more favorable circumstances.

**KEYWORDS:** UAPF, FPNSC, CFSOGI, SOLAR, POWER QUALITY.

**INTRODUCTION:** The final production, determines the social and economic progress usage, and management of energy of a country. Economical power

procurement is viewed as a practical way to reduce the output of non-renewable energy sources, reduce the usage of gasoline subsidiaries, and address the energy crisis as a whole. Due to its wider geographic distribution and ease of conversion into electrical energy through the use of photovoltaic (PV) boards, solar energy is more widely recognized than other sustainable power sources. At any rate, the energy of the sun is dependent on temperature and irradiance, both of which fluctuate and are often disturbed. These traits lead to variations in voltage and power, and when similar traits are introduced into the utility system, problems with power quality (PQ) and reliability become even more severe. For effective power change, it also needs power electronic converters and liberal control methods, which results in terrible voltage and current music. A few PQ problems have also been brought on by the transport structure's redundant power electronic-based nonlinear, inductive, and inconsistent weights. These can have detrimental impacts on electrical equipment, including over-burdening, increased misfortunes, bad influence, and the failure of protective devices and control circuits. In addition, the most prominent network side voltage quality problems are

utility organization voltage instabilities, such as hang, swell, and impedances. The system is susceptible to faults, lightning, short circuits, utility grid voltage variations, and turning on and off big loads or capacitor banks.

This causes significant financial losses by upsetting both basic and sensitive burdens. In [1] and [2], the PQ events, causes, and outcomes are genuine. Recently, specialists have merged innovative ideal models for PV-dealt with dynamic power channels (APFs) such as shunt APF (PV-ShAPF) [3]-[6] and series APF (PV-SeAPF) [7] to work on the concept of power close by the coordination of sun-based PV in a course system. However, it is clear that the aforementioned APFs' capabilities are limited because each one is designed to handle a particular set of power quality problems and is unable to support numerous modes of operation. To solve the aforementioned problems, PV-UAPF is created by combining Sh-APF and Se-APF at a standard DC interface and reconciling PV. It addresses power quality issues linked to voltage and current in the framework and can operate in various modes. Additionally, by efficiently balancing active and reactive electricity in the distribution network, the

PV-UAPF lessens the intermittent nature of solar PV. However, the productive activity of the PV-UAPF is heavily dependent on control methods. Numerous time-space control techniques that have been demonstrated to work have been written about for three-stage APFs. The main challenges in this scenario are determining a reference voltage and current amount based on the anticipated activity and management of APFs, as well as extracting stage point, recurrence, and important positive negative succession portions (FPNSC) in unfavorable lattice settings.

Traditional control solutions for UAPF include unit template theory, instantaneous symmetrical component theory, instantaneous reactive power theory, and synchronous reference frame theory. These methods are not appropriate for PV mix at DC interface with input capability to the structure and result in non-sinusoidal reference that adds up to APFs for PQ overhaul. Furthermore, when evaluating reference sums, low-pass channels (LPF) and ordinary repeat locked circles (FLL) are employed; these techniques will degrade the performance in irregular and distorted circumstances [10], [12].

## **II.LITARATURE SURVEY:**

### **1) Control of Grid Tied Smart PV-DSTATCOM System using an Adaptive Technique**

In order to decorate energy best and assist the three segment AC grid by using supplying power to the grid and the related hundreds, this paper proposes a control of smart PV (Photovoltaic)-DSTATCOM (Distribution Static Compensator) grid tied system the usage of an adaptive reweighted zero attracting (RZA) manipulate set of rules with P & O (Perturb and Observe) maximum electricity factor monitoring approach for a 3 segment device. The cautioned PV grid-tied device can perform constantly. The cautioned gadget serves as a DSTATCOM to enhance strength pleasant beneath sunny conditions, and it additionally distributes electricity from the PV array to the grid and cargo. Nonetheless, the recommended machine functions as a DSTATCOM to decorate energy satisfactory and transmit electricity from the grid to the weight at night or in overcast situations. The device is referred to as smart considering that it may feel PV power in both instructions and perform multidirectional strength waft. A evolved prototype is used in the lab for the experimental validation in an expansion of settings.

## 2) Enhanced Frequency-Locked Loop with a Comb Filter under Adverse Grid Conditions

Harmonic decoupling with multiple notch filters changed into commonly used to remove the harmonic additives so one can improve the overall performance of frequency-locked loops (FLLs) below distorted grid conditions. This technique, whilst providing exact filtering abilities, suffers from complexity, a high computational burden, and deteriorated dynamics. In order to cope with these issues, this letter suggests an advanced adaptive notch-filter out-primarily based FLL (ANF-FLL) that provides a comb filter and enhances filtering skills via adding absolutely imaginary zeros to accomplish harmonic cancellation and notch peaks. Compared to the traditional existing answers of more than one notch-clear out-based FLLs for distorted grid systems, the counseled FLL has a low computational load and a sincere production. Additionally, without compromising the FLL dynamics, it is able to absolutely block the DC element in addition to abnormal or even harmonics of the enter grid voltage. Comparisons and experimental findings are supplied to help the efficacy of the advised FLL.

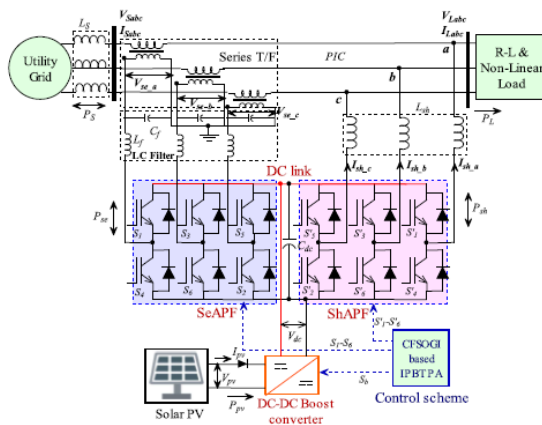
**III.PROPOSED SYSTEM:** In light of the recently mentioned perspective, a robust brush channel second-request summed up integrator (CFSOGI) based rapid power balance hypothesis power point (IPBTPA) regulator is typical for PV-UAPF in order to overcome the aforementioned obstacles. The enormous obligations and specific characteristics of the suggested work are as follows:

- 1) By combining power sources that are innocuous to the ecosystem, the structure provides a twofold reward method to further increase power quality using a CFSOGI-based IPBTPA control plot.
- 2) The suggested CFSOGI FLL isolates the repeat, stage point, and important positive and negative framework voltage components under switched or irregular settings.
- 3) Compared to other flexible scoring channels, the CFSOGI requires less computation and has superior consonant rejection capabilities. In contrast, IPBTPA has a straight construction, employs fewer channels, and operates with fewer alterations.
- 4) The IPBTPA control plan traverses the reference sums through the extraction of reference and weight powers. The two APFs are then coordinated by the hysteresis

controller to manage the show under better circumstances.

5) When the system voltage fluctuates, the force point in the storage voltage extraction is executed. Consequently, as the Se-APF shares the response power discomfort of the Sh-APF, the utilization factor increases.

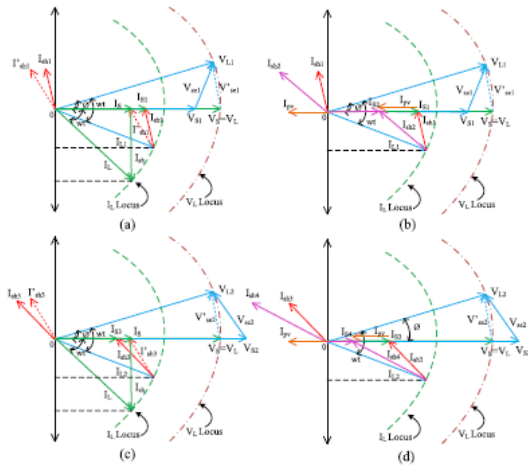
6) Under structure voltage impedances, the suggested control plot maintains the PV-UAPF. In this way, if sufficient power is available at the DC partner, the regulator is rather reasonable for islanded mode.



### 3.1 PV LINKED TO UAPF SYSTEM

Fig. 3.1 presents the suggested PV controlled UAPF game strategy. It has a two-way continuous compensating mechanism connected to a standard DC interface. "Shunt dynamic power channel" (Sh-APF) is the term created when a shunt inductor interfaces an equivalent voltage source converter (VSC) to the heap side. It functions as a continuously regulated

sinusoidal voltage source. The voltage source converter is referred to as a series dynamic power channel (Se-APF) since it is connected to the network via a series transformer. It functions similarly to a voltage-controlled source of sinusoidal current. A point of interconnection (PIC) is the term used to describe the indication of association. The converters' exchanging traces are covered by an LC channel. For receptive power interest, the direct R-L burden is taken into account, and for symphonious interest, the nonlinear burden is executed using a diode span rectifier that handles the RL load. Solar PV is connected to the electric grid via a two-stage conversion mechanism. In the basic stage, the sun-based PV is connected to the standard DC association using a lift converter. Then, the most notable power from the sun-based PV is isolated using the irritation and notice (P and O) computation. Dynamic power channels are used in the next step to accomplish the DC-AC power conversion. A robust CFSOGI-based IPBTPA control scheme is used to generate the exchanging beats for the converters by identifying numerous indicators as examination from the framework in order to operate and regulate the PV-UAPF structure in various situations.



### 3.2. Phasor representation of Sag and Swell with UAPF and PV-UAPF

In the current situation, VSabc and ISabc handle the three-stage (a, b, and c) source voltages and streams, while VLabc and ILabc tend to the stack voltages and streams. Ish\_a, Ish\_b, and Ish\_c are the separate shunt streams and series mixed voltages for the different stages. PS, PL, Psh, and Pse are the components of sources, loads, shunts, and series powers. Vdc and Ppv stand for DC interface voltage and PV power, respectively. The PV-UAPF's distinct phasor diagram is displayed in the figure under various conditions. 2. The following presumptions are taken into account when designing the phasor diagram: I) Inductive weight (ii) Ignored decrease in internal voltage (iii) Weight restrictions are used as a guide; (iv) sections that accumulate energy are forgiven; and (v) weight requests exceed the amount of power generated by sunlight.

Fig. 2(a)–(b) monitors the phasor graph of UAPF with and without PV under list conditions. Fig. 2(a) deals with the UAPF phasor chart in the droop situation. Prior to voltage hang, the UAPF adjusts for weight responsive power and symphonious balancing. Versus in this case is VL and IL jeans with a "wt" point.

The Sh-APF current, which is expected to offset the heap responsive power interest, is addressed by the continuous Ish. The voltage lowers from Versus to VS1 in the hang state, and the source must provide IS1 with greater current in order to comply with the dynamic power control. The Se-APF moves IL to IL1 in the IL locus and VL to VL1 in the VL locus by infusing the voltage Vse1 in order to achieve droop compensation. The drop-in source voltage does not manifest in the heap voltage as a result of the injected voltage maintaining its optimal degree of weight voltage. Similarly, in order for the Se-APF to blend Vse1 for hang pay and weight responsive power, the Sh-APF should pass the continuous Ish1 on. 3.2(a). As a result, Ish1 will start active power transfer between the Sh-APF, Se-APF, source, and load and efficiently regulate the DC link voltage. Similarly, the power point ( ) between the weight voltage and the source voltage shows the amount of

responsive power discomfort on Sh-APF that Se-APF shares in order to boost Se-APF utilization. The light-based PV is injected and the associated phasor frame is shown in hang state in Fig. 3.2(b). Here, the solar PV injects  $i_{pv}$  in opposition to the source current in phase. Less  $IS_2$  is taken from the source when the sun-based PV generates more distinct power. It then encourages the possibility of substantial hang compensation in any case for islanded mode.

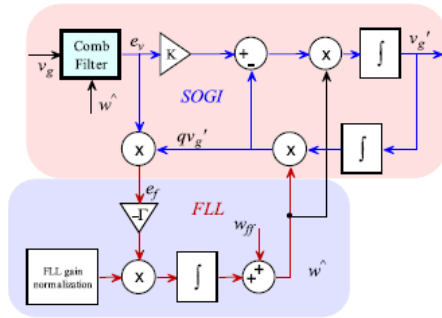
The phasor representation of UAPF in the developed state is shown in the figure. 3.2(c). The dynamic power control is finished when the source voltage Versus increases to  $VS_2$  and the information voltage  $IS$  falls to  $IS_3$ . To compensate for the larger situation, the Se-APF infuses the voltage  $V_{se2}$ . As a result, the heap voltage and load receptive power support are kept at the necessary levels. The new locations of the stack voltage and current are  $VL_2$  and  $IL_2$ , respectively, as shown in Figure. 3.2(c). This action should receive a continuous  $I_{sh3}$  from the Sh-APF. The hang state appears throughout the rest of the approach. The PV-UAPF phasor chart in its extended state is shown in the figure. 3.2(d). Under the droop condition, the Sh-APF and Se-APF borders are computed in a manner akin to that of the PV-UAPF. On the other hand, load power

during hang/expand situations is outstripped by sunshine-based power. The grid current shifts from its initial location to the left. It proves that the underutilized solar electricity is the responsibility of the utility system. In contrast, Sh-APF integrates most of the available sun-oriented power into the utility architecture in the same direction and uses responsive power to offset consonant concealment and burden. Here, load nearby hang/develop compensation is used by the Se-APF to share the open power interest. The DC connect voltage is actually controlled by this twofold remuneration method.

The CFSOGI, Sh-APF, and Se-APF regulators are largely included in the truly advanced control device. The PV-UAPF reduces power quality problems linked to voltage and current from a utility organization and weight perspective. The most substantial power extraction from the sun is used to achieve the sensible power offset. Under twisted or troublesome organization settings, the CFSOGI removes the major positive and negative bits of construction voltages ( $VS_{dq0}$ ) as well as the stage point (wt) from this control plot. The components that have been removed are used only to create the reference voltage and current signals for the Sh-APF and Se-APF



control schemes. Sh-APF and Se-APF regulators are coordinated by the IPBTPA control plot, which also generates varying heartbeats for the voltage source converters. In the next subsections, the comprehensive control strategy for PV-UAPF is thoroughly reviewed.



### 3.3 CFSOGI FLL REPRESENTATION

$$F(s) = \frac{1 - e^{-sT_w}}{4} \quad (1)$$

$$|F(jw)| = 0.5 \times \sin(wT_w/2), \angle F(jw) = \frac{\pi}{2} - \frac{wT_w}{2} \quad (2)$$

The previous condition, which indicates that the music repeats at  $f = 1/T_w$ , explains why the brush filter provides no increment at  $f = n/T_w$  (where  $n = 0, 1, 2, 3 \dots$ ). As a result, the odd, harmonics, and DC portion are completely hindered. The CFSOGI structure is framed by combining this with a regular SOGI. The proposed CFSOGI FLL's key format and model info signal ( $v_g$ ) are shown in the figure. 3.3. Figure 3.3 should show that the information signal's fast sign,  $v_g$ , is in-season; the quadrature signal,  $qv_g$ , contrasts stage by  $90^\circ$ ; and the screw up

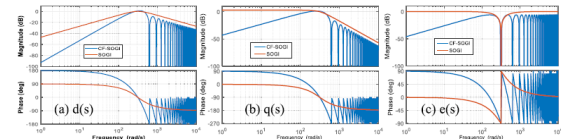
signal,  $ev$ . To remove consonant effects on the extraction of recurring and primary parts, the basic idea is to isolate a certain symphonious part and then subtract the retrieved music from the information sign. Conditions are used to define the exchange elements of  $d(s)$ , which deal with direct in-stage,  $q(s)$ , which deal with quadrature, and  $e(s)$ , which deal with error. (3)-(5).

$$d(s) = \frac{v_g'}{v_g} = \frac{(1 - e^{-sT_w})K\hat{w}s}{4(s^2 + \hat{w}^2)} \quad (3)$$

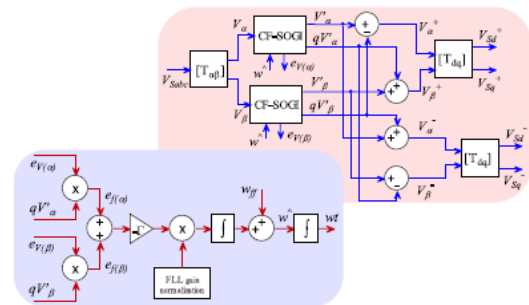
$$q(s) = \frac{qv_g'}{v_g} = \frac{(1 - e^{-sT_w})K\hat{w}^2}{4(s^2 + \hat{w}^2)} \quad (4)$$

$$e(s) = \frac{e_v}{v_g} = \frac{1 - e^{-sT_w}}{4} \quad (5)$$

$$\text{Gain - normalization} = \frac{K\hat{w}}{v^2 + qv^2} \quad (6)$$



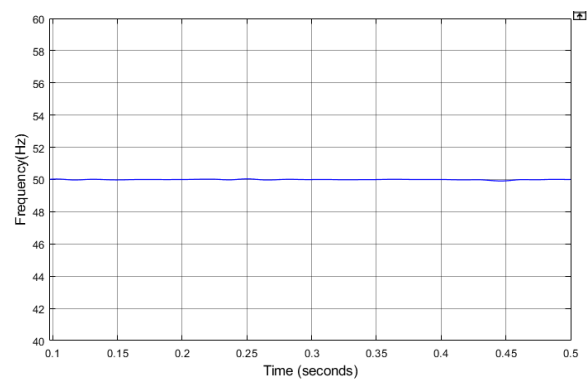
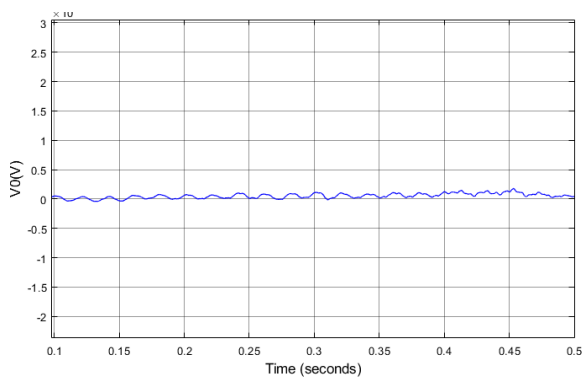
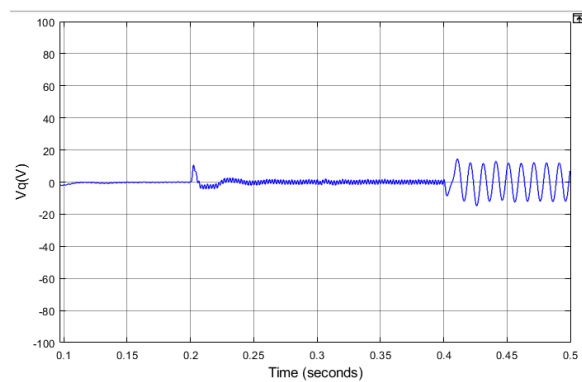
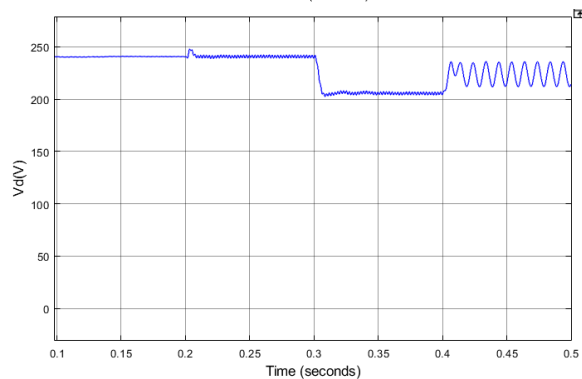
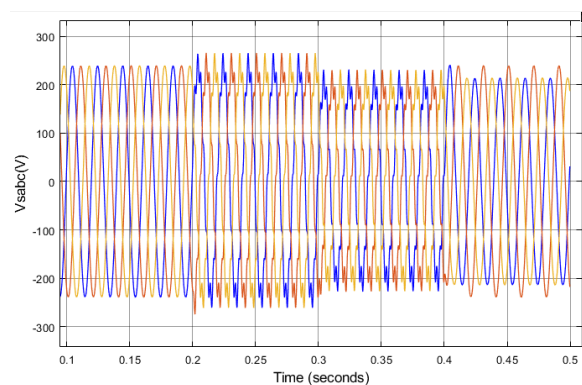
### 3.4 Frequency response plot of SOGI and CFSOGI



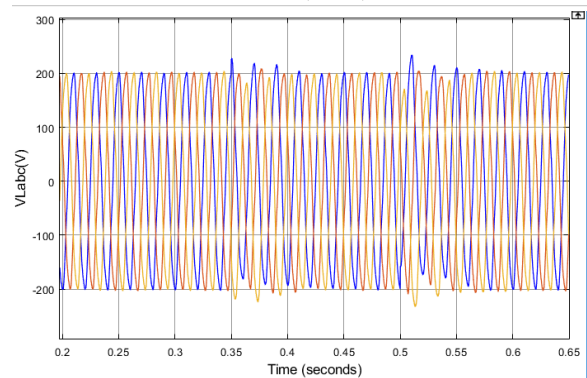
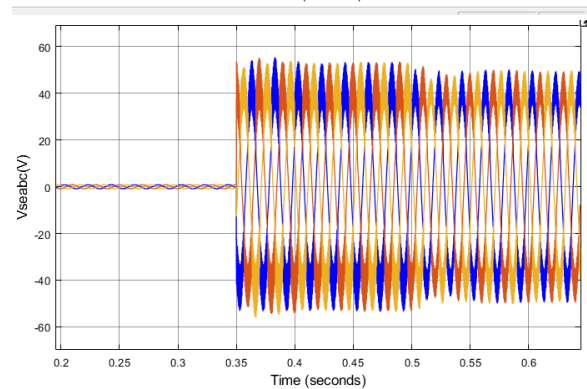
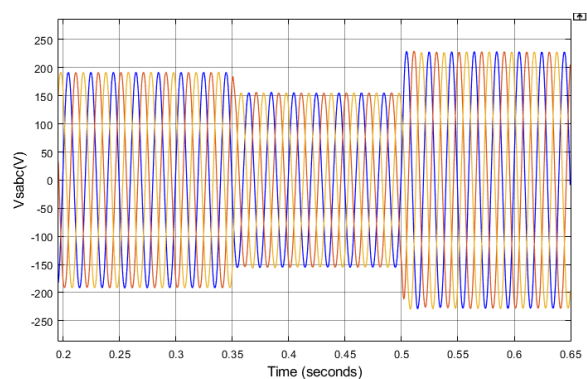
### 3.5 Extraction of fundamental d-q-0 and frequency components.

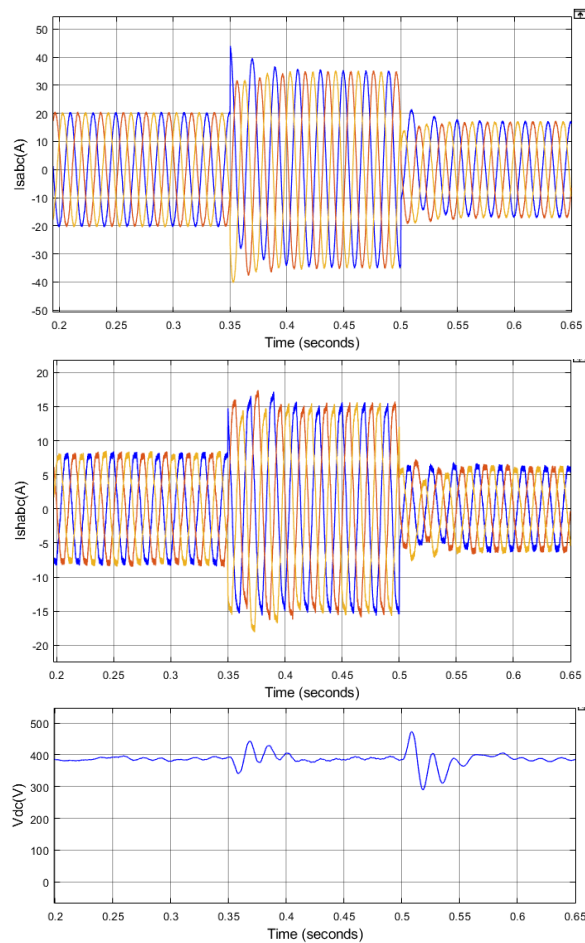
## IV.SIMULATION RESULTS:





#### 4.1 CFSOGI under grid voltage distortions.





3.2 under grid voltage fluctuation.

## V.CONCLUSION

This paper introduces a PV-UAPF with an IPBTPA control plot in light of CFSOGI. It provides consistency in power quality updates and determines the optimal power from sources that are safe for the environment. The activity and control of the suggested framework under various conditions have been investigated and examined using a comprehensive phasor graph. Phase angle, frequency, and basic components of grid voltages under normal or distorted conditions can all be extracted using the CFSOGI. It has amazing

consonant undoing powers by scoring tops at different symphonic frequencies. This is confirmed both numerically and using phasor plots and bode outline brilliance. Furthermore, regardless of PV, an IPBTPA regulator eliminates the reference voltage and current quantities for a double pay technique using simple burden assessment and reference power calculations. It demonstrates ubiquity by ensuring a framework's successful power homeostasis and DC interface voltage guideline. A greater converter utilization factor results from the two APFs sharing the reactive power. Under various test conditions, simulation aspects of the system's efficacy and performance are investigated. The findings are shown with reactive power compensation, power factor correction, harmonic suppression, and voltage fluctuation mitigation for different loads whose irradiance varies. In order to understand the microgrid/smart grid system, it is also supplemented with structure voltage obstacles for an islanded mode. For existing and remote districts in a course association, where persistent power quality problems arise despite the availability of numerous environmentally friendly power sources, the suggested work makes perfect sense.

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